



2823

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

JOZEF D. MITROS ET AL.

Serial No.. 10/020,034 (TI-32931)

Filed December 7, 2001

For: METHOD FOR FABRICATING LOW CHC
DEGRADATION MOSFET TRANSISTORS

Art Unit 2823

Examiner Long Pham

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

AMENDMENT UNDER 37 C.F.R. 1.111

In response to the Office action dated September 2, 2003, please amend the above
identified application as follows:

SEP 25 2003
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UNITED STATES PATENT AND TRADEMARK OFFICE